



How to Determine Whether Your CPU Has DSTNI or Altera Profibus Chip

Introduction

CTI processors employ a chip-based solution for operating Profibus networks. Over the course of the product life cycle to date, two different chip solutions have been used.

From 2007 to 2016, we used a “DTSNI” chip on all C200 / C300 / C400 CPUs. C100 CPUs do not have Profibus.

In 2016, we changed to an “Altera” chip due to obsolescence of the DSTNI chip.

CTI has released in October 2023 a new firmware release V9.25 that corrects some issues with the Profibus protocol for the CPU's that are built with the Altera chip.

Firmware release 9.25 can be installed on all CTI 2500 CPU's but it will have no effect on CPU's that are built with the DSTNI Chip.

All CTI 2500 Series **Compact CPU's** are built with the Altera Chip. Firmware V9.25 can also be applied to the Compact CPU's.

This paper shows four ways of determining if your CPU is equipped with a DSTNI chip or an Altera chip.

This paper is not applicable to Classic and Compact **JANUS** CPU's.

1. CPU serial number

If your CPU has a serial number:

from 279720 to 279727 included

starting from 279869 and all numbers above

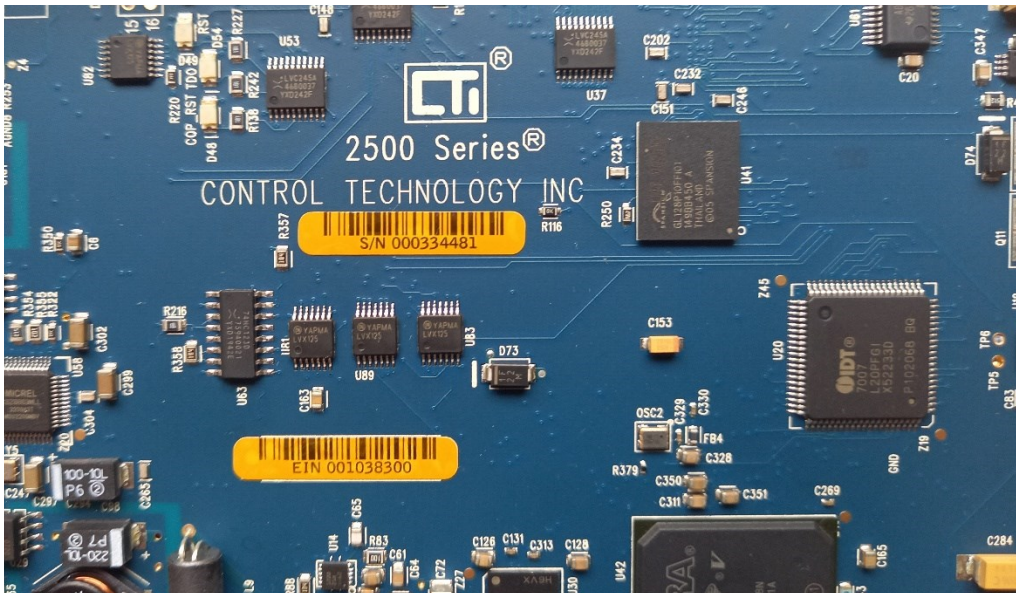
then it is built with an Altera chip.

All other serial numbers are built with the DSTNI chip.



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You can find the serial number on a yellow label affixed on the PCB board:



The serial number starts with S/N

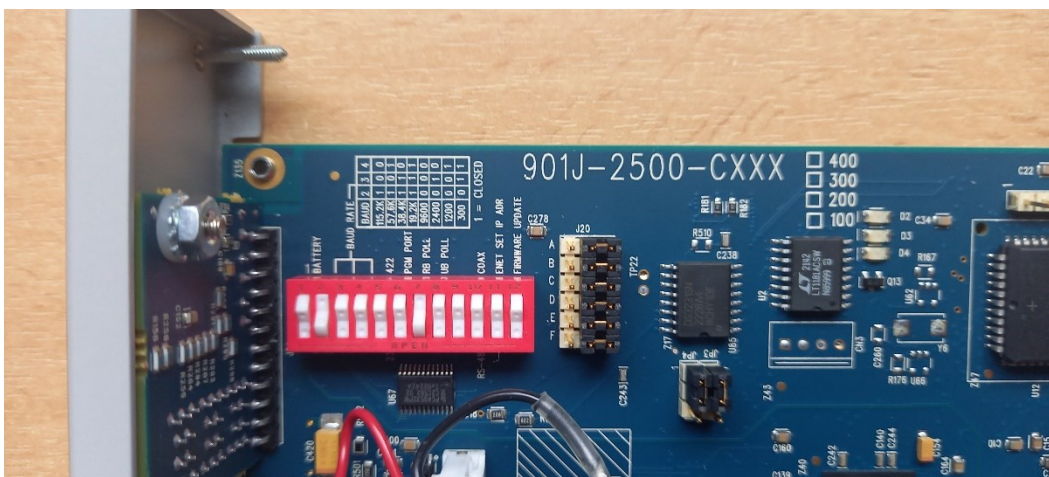
Do not confuse with the EIN number which is on another yellow label and is the Ethernet address.

When the CPU is installed in a base and under power you can also find the serial number in the Product Information web page. See below for an example.

2. Board Revision number

All CTI CPU's have a revision number of the printed circuit board.

The board revision number is printed on the PCB. The board revision number starts by the number 901 followed by a letter. The letter indicates the board revision.



If your CPU has a board revision of 901E or previous letters, then it has a DSTNI chip.

If you CPU has a board revision of 901G or later letters, then it has an Altera chip.



There is no board revision 901F.

3. Hardware configuration in Product Information

When the CPU is installed in a base and under power you can connect to the CPU by typing the IP address of the CPU in a web browser (Edge, Chrome, Firefox..) on a PC.

The PC will display the overview page of the integrated webserver in the CPU.

Click on the page Product Information.

In the Product Information page you will find the line Hardware configuration.

If the value of Hardware configuration is **0x1** then your CPU is equipped with an Altera chip.

If the value of Hardware configuration is **0x0** then your CPU is equipped with an DSTNI chip.

In this page you can also find the serial number of the CPU.

click Product Information

serial number

**0x1 = Altera System
0x0 = DSTNI System**

Product Information

This page allows you to view product information including the product serial number, c

Product Number	2500-C400
Product Name	CTI 2500 PLC
Serial Number	000285354
Manufacturing Date	01/17/2017
Ethernet MAC Address	00:20:25:0F:B3:37
IP Address	10.25.1.200 /24
Subnet Mask	255.255.255.0
Gateway	10.25.1.1
Hardware Configuration	0x1
Application Firmware Version	09.22
Application Firmware Date	10/27/21
I/O CPLD Version Number	06.02
Serial Port Baud Rate	115200
Dipswitch Settings	0x472
2 Serial Port Baud Rate-SW2	
3 Serial Port Baud Rate-SW3	
4 Serial Port Baud Rate-SW4	
5 Serial Port RS232/RS422	Open=RS232 Closed=
6 TCP Programming Port	Open=4452 Closed=1505/4450.. closed
7 Disable Standby Base Polling	Closed=Disable Polling.. closed
8 Disable Unconfigured Bases	Closed=Disable Bases.... closed
9 Reserved	open
10 Remote I/O Interface	Open=RS485 Closed=Coax..... open
11 Set IP Address via local port/web page	Closed=Enabled.... closed
12 Firmware Update	Closed=Enable..... open
User Jumper Settings	0x3e
A APT Memory (C400):	Open=1856K Closed=2048K..... closed
B Serial Port Use:	Open=Programming Closed=Printer Port.. open
C EnetPort I/F Mode:	Open=Fixed(MDI) Closed=Auto Crossover open
D Reset to default IP Address at boot	Closed=Reset..... open
E Reserved	open
F Reserved	open
Product Clock	Fri Oct 13, 2023 10:45:42.367



4. Profibus statistics web page

CPU's with a recent Firmware version incorporate a specific Profibus I/O Statistics web page in the Web server.

In the Profibus I/O Statistics Web page under Implementation, you will find the information if your CPU is equipped with a DSTNI chip or Altera chip.

2500 PLC Controller x +
Non sécurisé | 169.254.5.242/ProfibusStatistics

2500 Series™ PLC

Thu Oct 12 2023 17:58:57 **Profibus I/O Statistics**

[Main Menu](#)
[Event Log](#)
[Product Information](#)
[Network Configuration](#)
[PLC Scan Statistics](#)
[Icplc Statistics](#)
[Ethernet Statistics](#)
[I/O Statistics](#)
[Profibus I/O Statistics](#)
[Display All Statistics](#)
[Product Support](#)

Clear will zero counts and restart data collection.

Current Time: Thu Oct 12, 2023 16:59:23.742
Last 'Clear' Time: (not cleared since last PLC startup)

Profibus DP Statistics

Implementation	DSTni chip
Bus Params Loaded	Yes
Operation Mode	Operate
Master State	Normal Operation
Max Poll Cycle End Wait Msecs	409
STW147 Count (slave not responding)	65535
STW148 Count (master state not OK)	0
Successive Slave Download Failures	0
Successive Diagnostic Retrieval Failures ..	0
Number of Configured Slaves	7
Number of Diagnostic Events From All Slaves	8
Time of Last Diagnostic Event	Thu Oct 12, 2023 14:41:22.049, from slave 10
Station Addresses of Offline Slaves	3 4 6 7 8 9 10
sys_os_lock() Statistics:	

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